DERWENT PUBLICATIONS

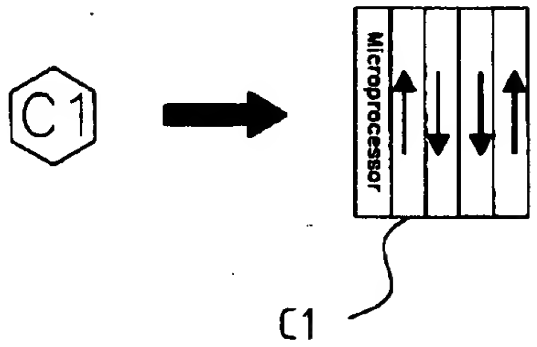

Fig 1

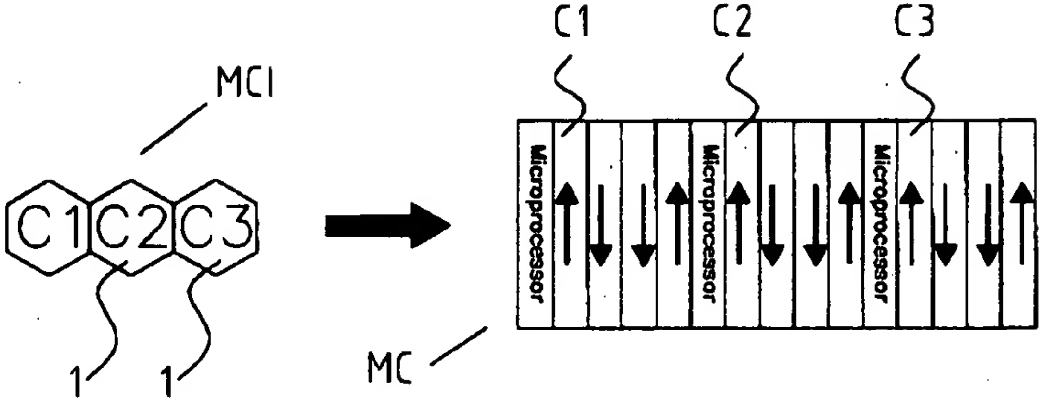

Fig 2

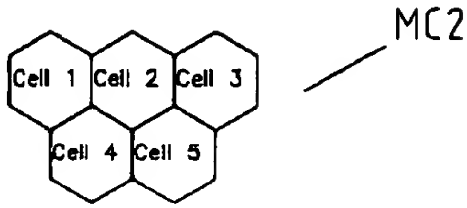

Fig 3

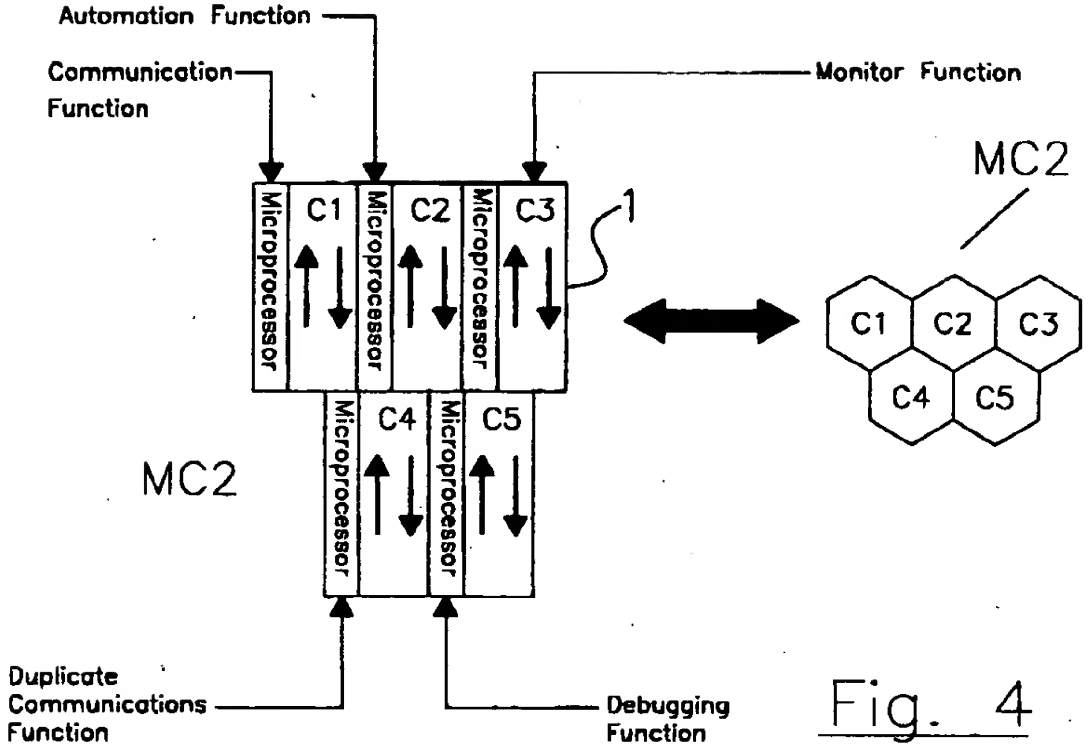
Automation Function
Communication Function
Monitor Function
Microprocessor
C1
C2
C3
C4
C5
1
MC2
MC2
Duplicate Communications Function
Debugging Function

Fig. 4

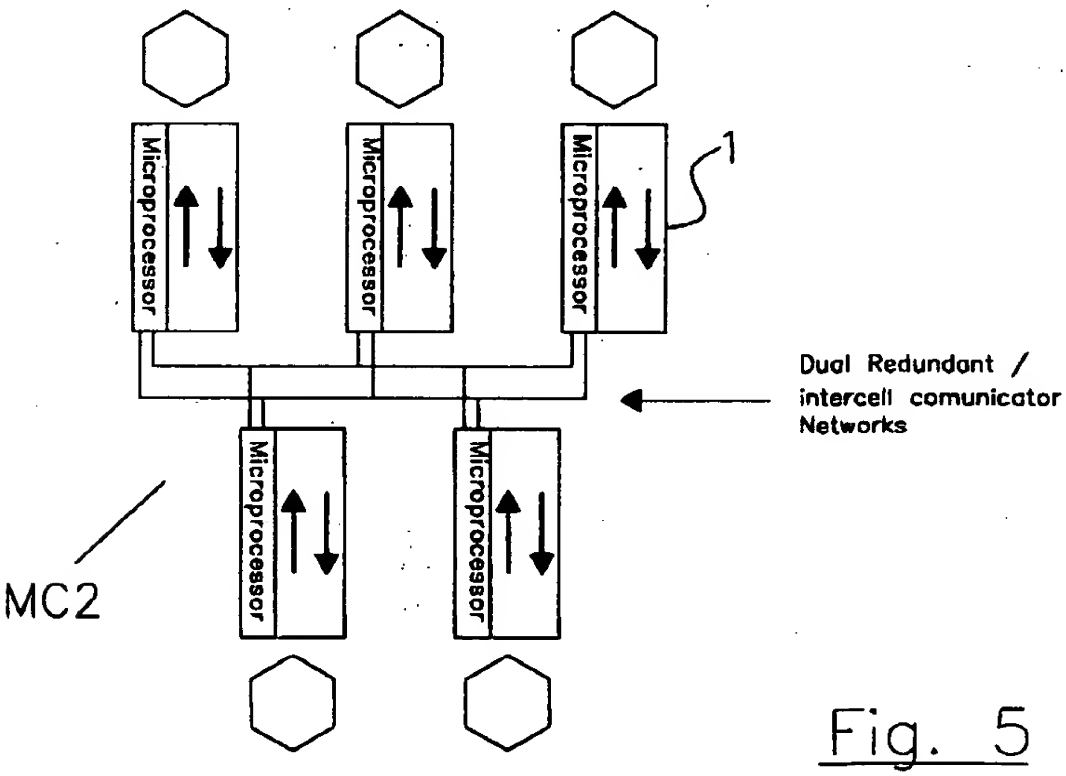
Microprocessor
1
Dual Redundant / intercell comunicator Networks
MC2

Fig. 5

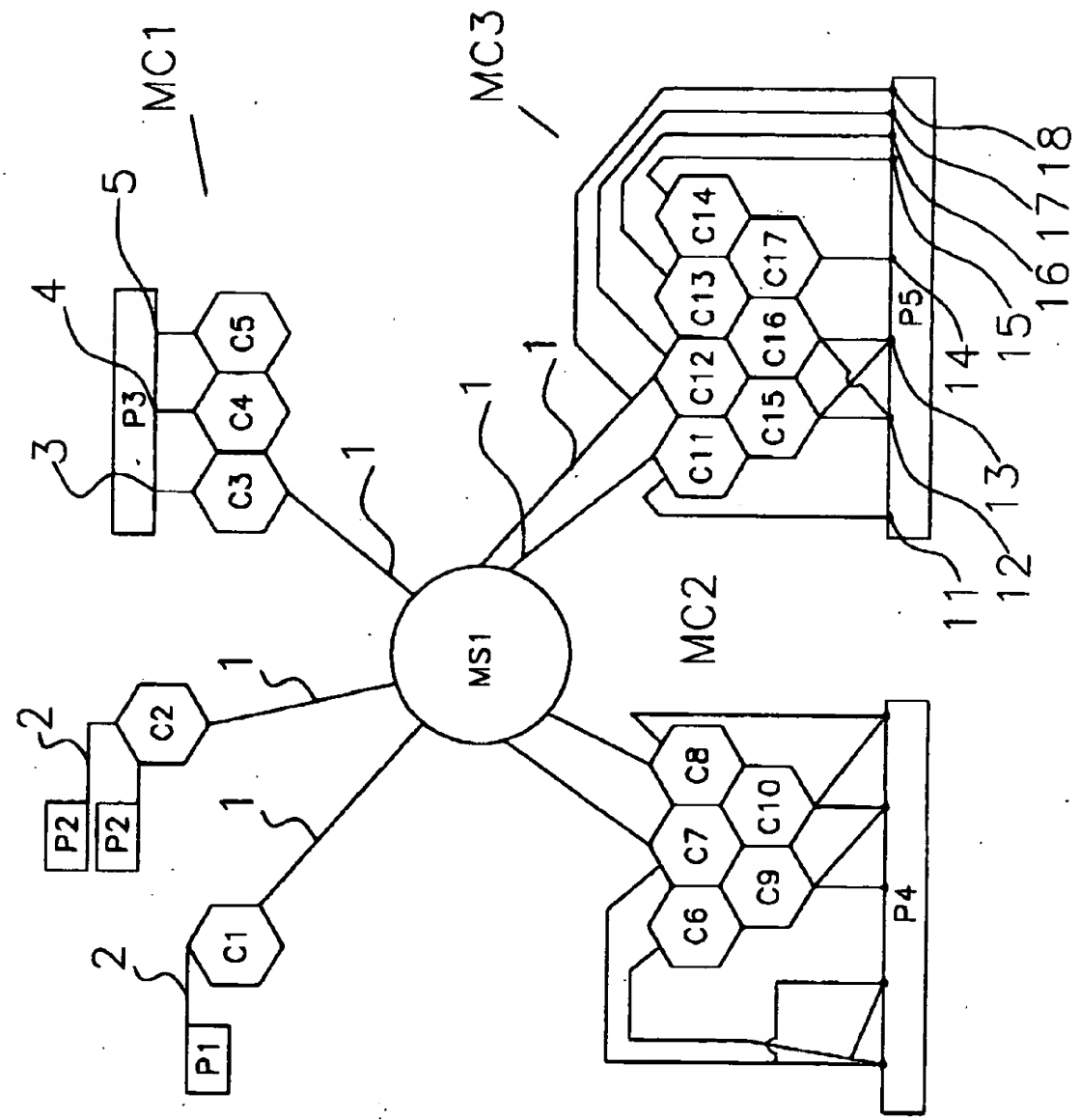
MC1
MC3
MC2
MS1
P1
P2
P2
P3
P4
P5
C1
C2
C3
C4
C5
C6
C7
C8
C9
C10
C11
C12
C13
C14
C15
C16
C17
1
2
3
4
5
11
12
13
14
15
16
17
18

Fig. 6